

What is claimed is:

1. A multilayer capacitor comprising:  
dielectric layers and  
5 at least four types of, that is, first to  
fourth, internal conductor layers insulated from one  
another by said dielectric layer and arranged in an order  
from the first to fourth ones in a dielectric body;  
wherein  
10 each of said first to fourth internal  
conductor layers is formed with at least one cut part;  
said internal conductor layers are formed  
with channel parts for current to flow in return by said  
respective cut parts; and  
15 the channel parts in said internal conductor  
layers adjoining each other across said dielectric layer  
in the stacking direction carry current flowing in the  
reverse directions from each other.
- 20 2. The multilayer capacitor as set forth in  
claim 1, wherein plane shapes of said first internal  
conductor layer and said third internal conductor layer  
are symmetric each other with respect to the center of  
them.

3. The multilayer capacitor as set forth in claim 2, wherein plane shapes of said second internal conductor layer and said fourth internal conductor layer are symmetric each other with respect to the center of  
5 them.

4. The multilayer capacitor as set forth in claim 3, wherein  
said first internal conductor layer has a  
10 first lead part led to a first side surface of said dielectric body; and  
said third internal conductor layer has a third lead part led to a third side surface being opposite of said first side surface of said dielectric  
15 body.

5. The multilayer capacitor as set forth in claim 4, wherein  
said second internal conductor layer has a  
20 second lead part led to a second side surface being different from the first side surface and the third side surface of said dielectric body; and  
said fourth internal conductor layer has a fourth lead part led to a fourth side surface on the  
25 opposite side of said second side surface of said

dielectric body.

6. The multilayer capacitor as set forth in claim 5, wherein

5 a width of said first lead part is substantially the same with the entire width of the first internal conductor layer formed with said cut part; and  
a width of said third lead part is substantially the same with the entire width of the third  
10 internal conductor layer formed with said cut part.

7. The multilayer capacitor as set forth in claim 6, wherein

the first side surface of said dielectric  
15 body is attached with a first terminal electrode connected to said first lead part; and

the third side surface of said dielectric body is attached with a third terminal electrode connected to said third lead part.

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8. The multilayer capacitor as set forth in claim 7, wherein

said first terminal electrode and said third terminal electrode have the same or wider width compared  
25 with those of said first lead part and third lead part.

9. The multilayer capacitor as set forth in claim 8, wherein

the second side surface of said dielectric  
5 body is attached with a second terminal electrode  
connected to said second lead part; and

the fourth side surface of said dielectric  
body is attached with a fourth terminal electrode  
connected to said fourth lead part.

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10. The multilayer capacitor as set forth in claim 9, wherein

a width of said second lead part is  
substantially the same as that of said channel part  
15 separated by the cut part of said second internal  
conductor layer; and

a width of said fourth lead part is  
substantially the same as that of said channel part  
separated by the cut part of said fourth internal  
20 conductor layer.

11. The multilayer capacitor as set forth in claim 10, wherein

said second lead part is led to the  
25 substantial center portion of said second side surface;

and

said fourth lead part is led to the substantial center of said fourth side surface.

5           12.    The multilayer capacitor as set forth in claim 11, wherein

a width of said second terminal electrode is substantially the same as or wider than that of said second lead part but narrower than that of said second  
10 side surface; and

a width of said fourth terminal electrode is substantially the same as that of said second terminal electrode.

15           13.    The multilayer capacitor as set forth in any one of claims 1 to 12, wherein said first to fourth internal conductor layers are stacked in this order repeatedly for a plurality of times in the stacking direction respectively across said dielectric layers.

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14.    The multilayer capacitor as set forth in any one of claims 1 to 12, wherein a plane shape of said cut part has a substantial L-shape.

25           15.    The multilayer capacitor as set forth in

claim 14, wherein a width of said cut part is  $1/10$  to  $1/3$  of a width of said internal conductor layer.

16. A multilayer capacitor comprising:  
5 dielectric layers and  
at least eight types of, that is, first to  
eight, internal conductor layers insulated from one  
another by said dielectric layer and arranged in an order  
from the first to eighth ones in a dielectric body;  
10 wherein  
each of said first to eighth internal  
conductor layers is formed with at least one cut part;  
each of said internal conductor layers is  
formed with a channel part for a current to flow in  
15 return by said cut part; and  
the channel parts in said internal conductor  
layers adjoining each other across said dielectric layer  
in the stacking direction carry current flowing in the  
reverse directions from each other.

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17. The multilayer capacitor as set forth in  
claim 16, wherein  
plane shapes of said first internal conductor  
layer and said fifth internal conductor layer are  
25 symmetric each other with respect to the center of them;

plane shapes of said second internal conductor layer and said sixth internal conductor layer are symmetric each other with respect to the center of them;

5 plane shapes of said third internal conductor layer and said seventh internal conductor layer are symmetric each other with respect to the center of them; and

plane shapes of said fourth internal conductor layer and said eighth internal conductor layer are symmetric each other with respect to the center of them.

18. The multilayer capacitor as set forth in  
15 claim 16, wherein

said first internal conductor layer has a first lead part led to the first side surface of said dielectric body;

said second internal conductor layer has a  
20 second lead part led to a different position from said first lead part on the first side surface of said dielectric body;

said fifth internal conductor layer has a fifth lead part led to the third side surface on the  
25 opposite side of the first side surface of said

dielectric body;

said sixth internal conductor layer has a sixth lead part led to a different position from said fifth lead part on the third side surface of said

5 dielectric body;

said third internal conductor layer has a third lead part led to the second side surface being different from the first side surface and the third side surface of said dielectric body;

10 said fourth internal conductor layer has a fourth lead part led to a different position from said third lead part on the second side surface of said dielectric body;

said seventh internal conductor layer has a  
15 seventh lead part led to the fourth side surface on the opposite side of the second side surface of said dielectric body; and

said eighth internal conductor layer has a eighth lead part led to a different position from said  
20 seventh lead part on the fourth side surface of said dielectric body.

19. The multilayer capacitor as set forth in claim 18, wherein a width of each of said lead parts is  
25  $1/3$  to  $1/4$  of a width of said channel part in each of



said internal conductor layers.

20. The multilayer capacitor as set forth in claim 18, wherein

5 a first terminal electrode and a second terminal electrode respectively connected to said first lead part and second lead part are attached to the first side surface of said dielectric body;

a third terminal electrode and a fourth terminal electrode respectively connected to said third lead part and fourth lead part are attached to the second side surface of said dielectric body;

a fifth terminal electrode and a sixth terminal electrode respectively connected to said fifth lead part and sixth lead part are attached to the third side surface of said dielectric body; and

a seventh terminal electrode and an eighth terminal electrode respectively connected to said seventh lead part and eighth lead part are attached to the fourth side surface of said dielectric body.

21. The multilayer capacitor as set forth in any one of claims 16 to 20, wherein said first to eighth internal conductor layers are stacked in this order repeatedly for a plurality of times in the stacking

direction respectively across said dielectric layers.

22. The multilayer capacitor as set forth in any one of claims 16 to 20, wherein a plane shape of said cut  
5 part is a substantial linear shape.

23. The multilayer capacitor as set forth in claim 22, wherein a width of said cut part is  $1/10$  to  $1/3$  of a width of said internal conductor layer.

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24. A multilayer capacitor comprising:  
dielectric layers, and  
at least four types of, that is, first to fourth, internal conductor layers insulated from one  
15 another by said dielectric layer and arranged in an order from the first to eighth ones in a dielectric body,  
a fifth internal conductor layer formed on the dielectric layer formed with said first internal conductor layer, adjacent to said first internal  
20 conductor layer on the same plane to be a pattern isolated from said first internal layer;

a sixth internal conductor layer formed on the dielectric layer formed with said second internal conductor layer, adjacent to said second internal  
25 conductor layer on the same plane to be a pattern

isolated from said second internal layer;

a seventh internal conductor layer formed on  
the dielectric layer formed with said third internal  
conductor layer, adjacent to said third internal  
5 conductor layer on the same plane to be a pattern  
isolated from said third internal layer; and

an eighth internal conductor layer formed on  
the dielectric layer formed with said fourth internal  
conductor layer, adjacent to said fourth internal  
10 conductor layer on the same plane to be a pattern  
isolated from said fourth internal layer;

wherein

each of said first to eighth internal  
conductor layers is formed with at least one cut part;

15 each of said internal conductor layers is  
formed with a channel part for a current to flow in  
return by said cut part; and

the channel parts in said internal conductor  
layers adjoining each other across said dielectric layer  
20 in the stacking direction carry current flowing in the  
reverse directions from each other.

25. The multilayer capacitor as set forth in  
claim 24, wherein

25 plane shapes of said first internal conductor

layer and said third internal conductor layer are symmetric each other with respect to the center of them;

plane shapes of said second internal conductor layer and said fourth internal conductor layer  
5 are symmetric each other with respect to the center of them;

plane shapes of said fifth internal conductor layer and said seventh internal conductor layer are symmetric each other with respect to the center of them;  
10 and

plane shapes of said sixth internal conductor layer and said eighth internal conductor layer are symmetric each other with respect to the center of them.

15           26.   The multilayer capacitor as set forth in claim 24, wherein

plane shapes of said first internal conductor layer and said fifth internal conductor layer are symmetric each other with respect to the center of a  
20 space between them;

plane shapes of said second internal conductor layer and said sixth internal conductor layer are symmetric each other with respect to the center of a space between them;

25           plane shapes of said third internal conductor

layer and said seventh internal conductor layer are symmetric each other with respect to the center of a space between them; and

said fourth internal conductor layer and said  
5 eighth internal conductor layer are symmetric each other with respect to the center of a space between them.

27. The multilayer capacitor as set forth in claim 24, wherein

10 said first internal conductor layer has a first lead part led to the first side surface of said dielectric body;

said fifth internal conductor layer has a fifth lead part led to the third side surface on the  
15 opposite side of the first side surface of said dielectric body;

said second internal conductor layer has a second lead part led to a different position from said first lead part on the first side surface of said  
20 dielectric body;

said sixth internal conductor layer has a sixth lead part led to a different position from said fifth lead part on the third side surface of said dielectric body;

25 said third internal conductor layer has a

third lead part led to a different position from said fifth lead part and sixth lead part on the third side surface of said dielectric body;

said seventh internal conductor layer has a  
5 seventh lead part led to a different position from said first lead part and second lead part on the first side surface of said dielectric body;

said fourth internal conductor layer has a fourth lead part led to a different position from said  
10 third lead part, fifth lead part and sixth lead part on the third side surface of said dielectric body; and

said eighth internal conductor layer has a eighth lead part led to a different position from said first lead part, second lead part and seventh lead part  
15 on the first side surface of said dielectric body.

28. The multilayer capacitor as set forth in claim 27, wherein a width of each of said first to eighth lead parts is the same as or narrower than that of the  
20 channel part in each of said internal conductor layers.

29. The multilayer capacitor as set forth in claim 27, wherein

said dielectric body has a parallelepiped  
25 shape having said second side surface and fourth side

surface being different from said first side surface and third side surface; and

widths of the first side surface and third side surface are wider than those of said second side surface and fourth side surface.

30. The multilayer capacitor as set forth in claim 29, wherein

the first side surface of said dielectric body is attached with a first terminal electrode connected to said first lead part, a second terminal electrode connected to said second lead part, a seventh terminal electrode connected to said seventh lead part and an eighth terminal electrode connected to said eighth lead part; and

the third side surface of said dielectric body is attached with a third terminal electrode connected to said third lead part, a fourth terminal electrode connected to said fourth lead part, a fifth terminal electrode connected to said fifth lead part and an sixth terminal electrode connected to said sixth lead part.

31. The multilayer capacitor as set forth in any one of claims 24 to 30, wherein said first to fourth

internal conductor layers are stacked in this order repeatedly for a plurality of times in the stacking direction respectively across said dielectric layers; and said fifth to seventh internal conductor layers are stacked in this order repeatedly for a plurality of times in the stacking direction respectively across said dielectric layers.

32. The multilayer capacitor as set forth in any one of claims 24 to 30, wherein

said cut parts formed on said first, fifth, third and seventh internal conductor layers have a substantial L-shape; and

said cut parts formed on said second, sixth, fourth and eighth internal conductor layers have a substantial linear shape.

33. The multilayer capacitor as set forth in claim 32, wherein a width of said cut part is  $1/10$  to  $1/3$  of a width of said internal conductor layer.